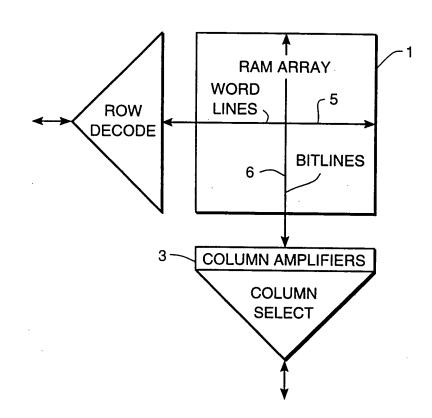
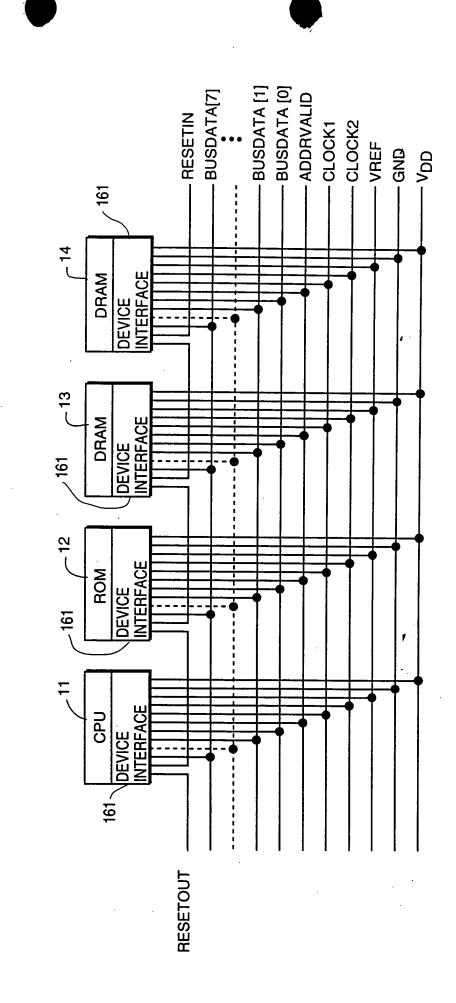


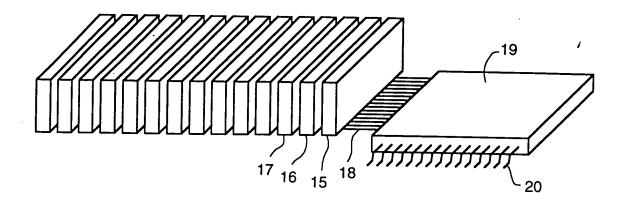
6304937

FIG_1

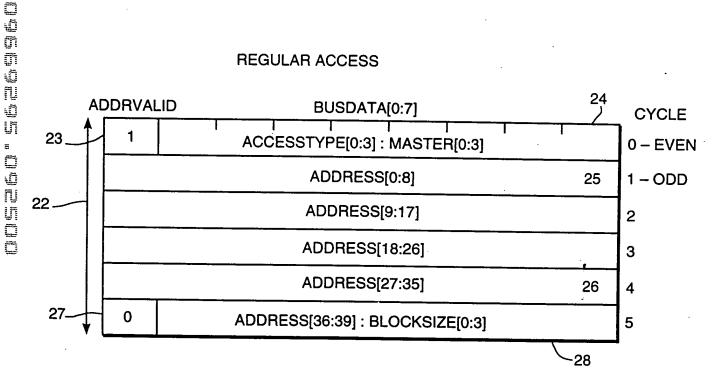






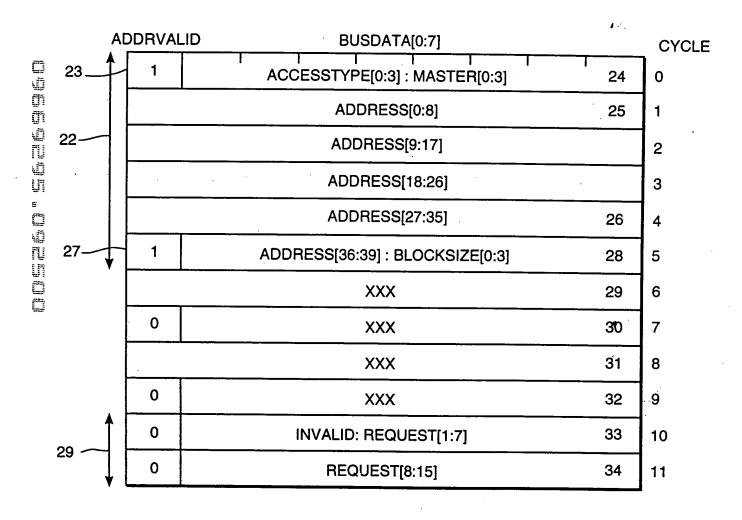


REGULAR ACCESS

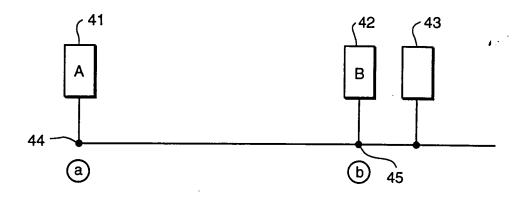




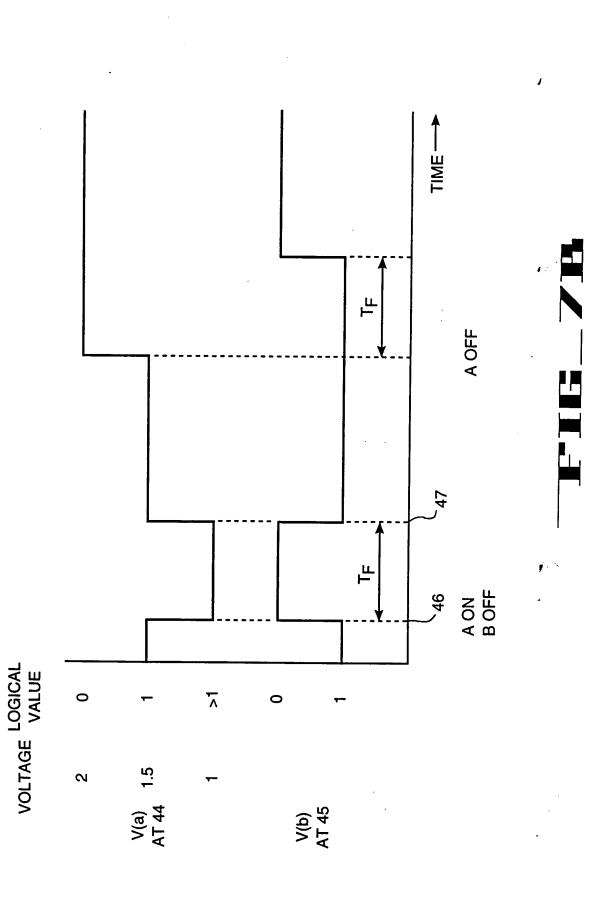
Α	DDRVALID		BUSDATA[0:7]					CYCLE
23	1	ACCESSTYPE[0:3] : MASTER[0:3] = 0 INFO[0:7]						0 - EVEN
28	0							1 – ODD

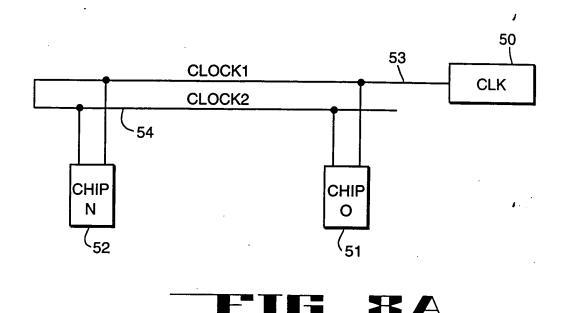


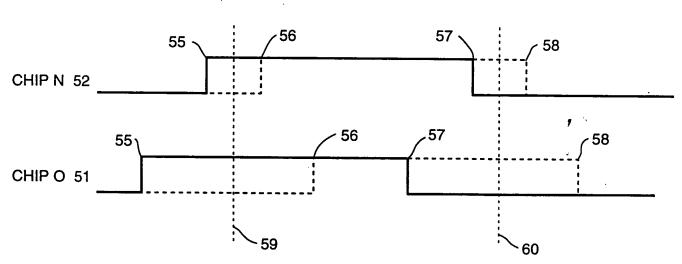




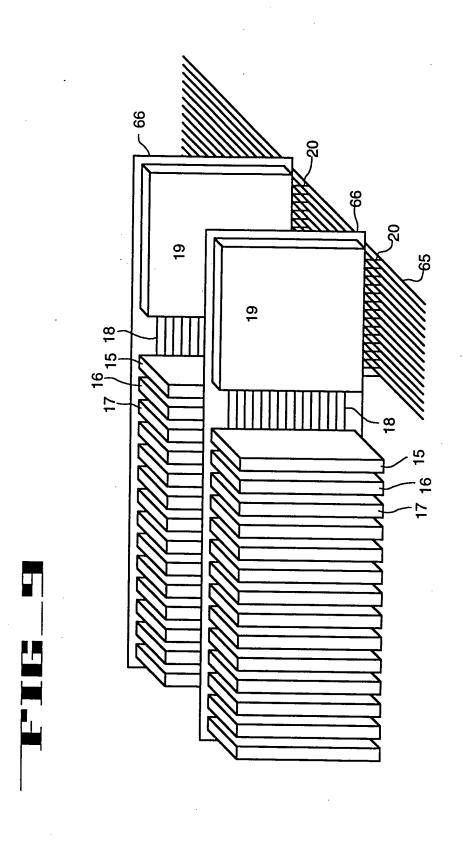
TIG_7A

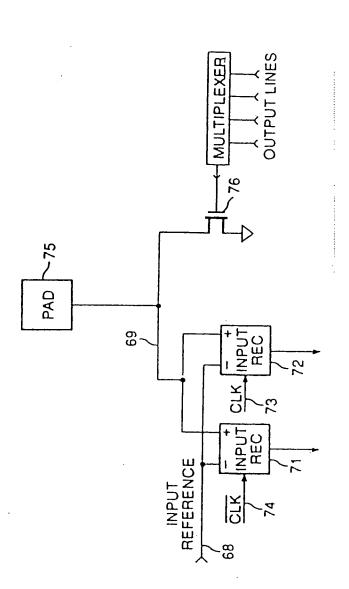




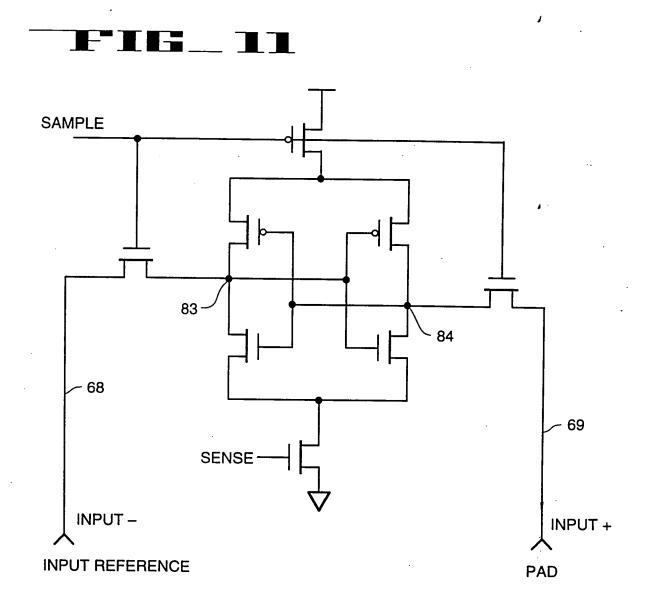


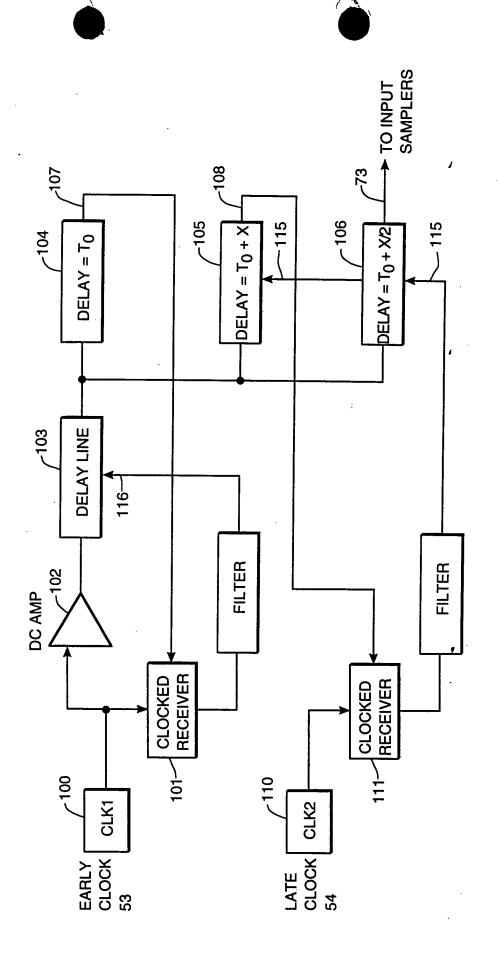
FIG_8B

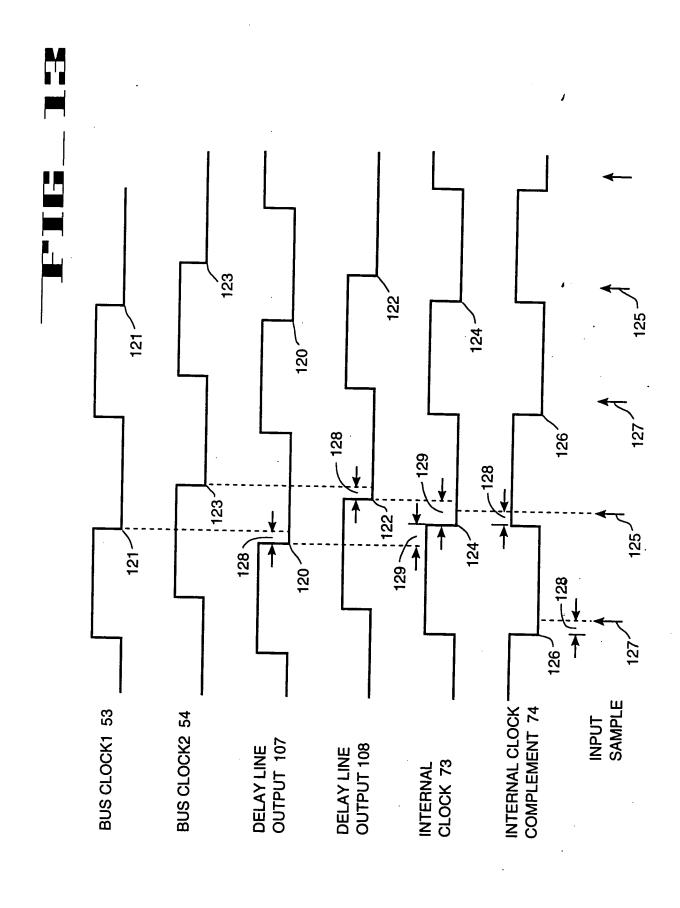


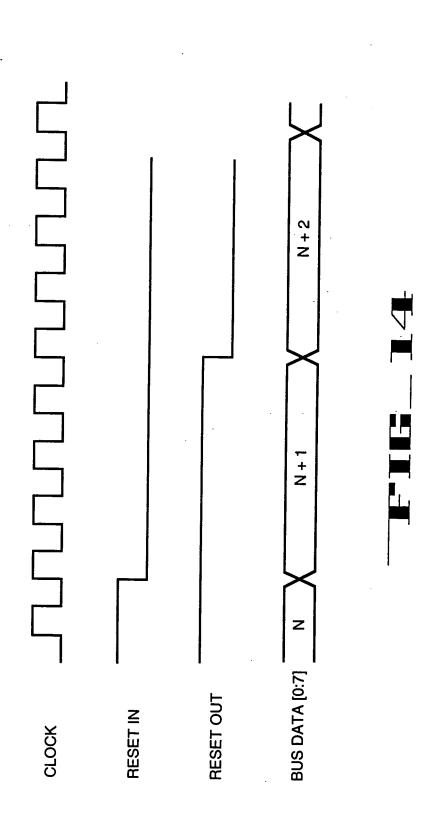


F/G. 10

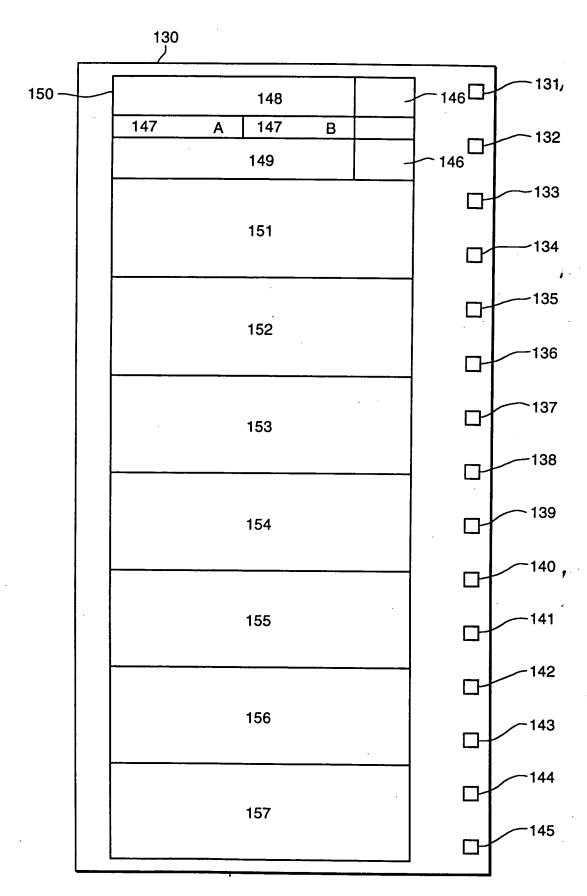








FIG_15



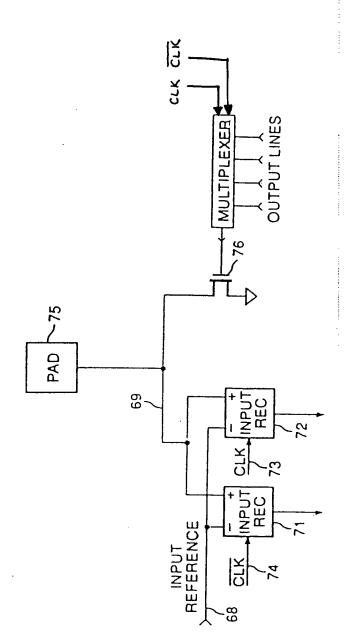


FIG. 10

